

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Wonju CHO, et al.

Serial No.: New Application Group Art Unit: Not Yet Assigned

Filed: October 15, 2001 Examiner: Not Yet Assigned

Title: ULTRA SMALL SIZE VERTICAL MOSFET DEVICE AND METHOD FOR  
THE MANUFACTURE THEREOF

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents  
Washington, DC 20231

October 15, 2001

Sir:

As a means of complying with the duty of disclosure under 37 CFR §1.56, and in accordance with 37 CFR §§1.97 and 1.98, Applicant(s), through the undersigned attorney, submits this Information Disclosure Statement. The patents, publications or other information submitted herewith are listed on the attached Form PTO-1449 and copies are attached.

In accordance with 37 CFR §1.97(b)(1) or (2), this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, or within three months of the date of entry into the national stage of the above-identified application as set forth in 37 CFR §1.491. Accordingly, no fee is required.

Respectfully submitted,

JACOBSON HOLMAN, PLLC

By: \_\_\_\_\_

Yoon S. Ham

Registration No. 45,307

400 Seventh Street, N.W.  
Washington, DC 20004  
(202) 638-6666

Atty. Dkt. No.: P67201US0  
YSH:ecl

JACOBSON, PRICE, HOLMAN & STERN, PLLC  
 400 SEVENTH STREET, N.W.  
 WASHINGTON, D.C. 20004-2201

## LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.: P67201US0 GROUP ART UNIT: Not Yet Assigned  
 SERIAL NO.: Not Yet Assigned FILING DATE: October 15, 2001  
 APPLICANT(S): Wonju CHO, et al. TODAY'S DATE: October 15, 2001

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 U.S. PATENT DOCUMENTS

*EXAMINER <u>INITIAL</u>	DOCUMENT NUMBER	DATE	NAME	INT'L CLASS	SUB- CLASS	FILING DATE (If Appropriate)
AA	<u>5,365,097</u>	<u>11/15/94</u>	<u>Kenney</u>	<u>H01L</u>	<u>29/68</u>	<u>10/05/92</u>
AB	<u>5,382,816</u>	<u>01/17/95</u>	<u>Mitsui</u>	<u>H01L</u>	<u>29/10</u>	<u>05/28/93</u>
AC	_____	_____	_____	_____	_____	_____

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 FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION (YES) (NO)
AD	_____	_____	_____	_____	_____
AE	_____	_____	_____	_____	_____

\*\*\*\*\*  
 OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AF	<u>Hergenrother, et al.; The Vertical Replacement-Gate (VRG) MOSFET: A 50-nm....; 1999 IEEE; p.99-75 - 99-78.</u>
AG	<u>Hergenrother, et al.; The Vertical Replacement-Gate (VRG) MOSFET With ....; 2000 Extended Abstract of ....; p. 220-221.</u>
AH	_____

EXAMINER

DATE CONSIDERED

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).